Τμήμα Μηχανικών Η/Υ & Πληροφορικής - Σεμινάρια 2021-2022

ΔΙΑΛΕΞΗ

" Applications of Machine Learning in the Design of Secure and Trusted Integrated Circuits and Systems "

Γεώργιος Μακρής

Professor

Department of Electrical and Computer Engineering

Erik Jonsson School of Engineering & Computer Science

The University of Texas at Dallas

Περίληψη – Abstract

In this presentation, Prof. Makris will provide an overview of his research group's activities in applying machine learning and statistical analysis to a variety of problems related to security, trustworthiness and reliability of integrated circuits and systems, with particular emphasis in the analog/RF domain, including post-manufacturing test and calibration, hardware Trojan detection, counterfeit identification, provenance attestation, hardware-based malware detection and workload execution forensics.

Yiorgos is a professor in the Electrical and Computer Engineering department at the Erik Jonsson School of Engineering & Computer Science at The University of Texas at Dallas, which he joined in July 2011. Prior to that, he spent 10.5 years as a faculty of Electrical Engineering and of Computer Science at Yale University. He holds a Ph.D. (2001) and an M.S. (1997) in Computer Engineering from the University of California, San Diego, and a Diploma of Engineering (1995) in Computer Engineering and Informatics from the University of Patras, Greece.

Yiorgos is a Co-Founder and Site-PI of the NSF Industry University Cooperative Research Center on Hardware and Embedded System Security and Trust (NSF CHEST I/UCRC), as well as the Leader of the Safety, Security and Healthcare Thrust of the Texas Analog Center of Excellence (TxACE) and the Director of the Trusted and RELiable Architectures (TRELA) Research Laboratory. His research activities have been supported by NSF, SRC, ARO, AFRL, AFWERX, DARPA, DoE/Honeywell, Boeing, Northrop Grumman, KBR/Wyle, IBM, LSI, Intel, AMS, Advantest, Qualcomm and TI.

Τετάρτη 22 Ιουνίου 2022 – 11.30-13.30

Αίθουσα Σεμιναρίων

ΤΜΗΥΠ