

Discuss how processes can synchronize

For example, agree on the ordering of events, or avoid accessing a shared resource simultaneously

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Topics to be covered

Clock Synchronization Logical Clocks Global State Election Algorithms Mutual Exclusion Distributed Transactions

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Model

Assume we have N processes p_i (i = 1, 2, ..., N)

Each process

- executes on a single processor
- has a state that changes as it executes

 executes a series of actions (either a message send or receive operation or an internal operation of the process (e.g., update of one of its variables)

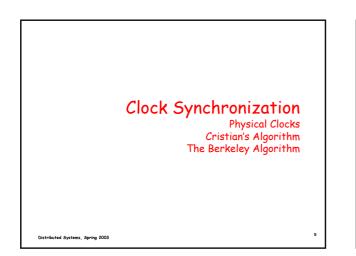
Event: the occurrence of a single action

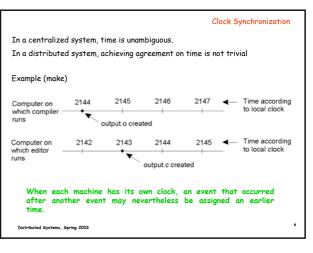
Events within a single process p_i can be placed in a single total order \rightarrow_i

Each process is characterized by its history, a series of events that occur at each process.

h_i = <e⁰, e¹, e², ...>

s;0: initial state





Physical Clocks

• Is it possible to synchronize all clocks in a distributed system?

Each computer has a circuit for keeping track of time

clock - timer a quartz crystal, when kept under tension, quartz crystals oscillate at a well-defined frequency

A counter & holding register: the counter is decremented by one at each crystal oscillation, when it gets to zero, an interrupt (clock tick) the counter is reloaded from the register

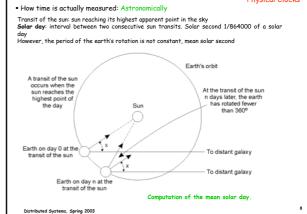
Can be programmed to give an interrupt say 60 times a sec

(software) clock: each interrupt adds 1 to the time stored in memory

With a single computer and a single clock, does not matter if the clock is off by a small amount – all processes use the same clock

Clock skew: difference in time values between the software clocks

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 Physical Clocks

 • How time is actually measured: Atomic Time: Counting transitions of the cesium 133 atom

 Universal Coordinated Time (UTC)

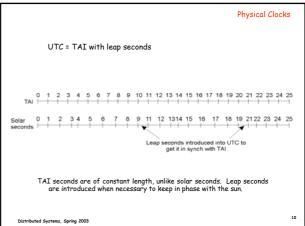
 Based on the number of transitions per second of the cesium 133 atom (1 sec = time it takes to make 9,192,631,770 transitions

 At present, the real time is taken as the average of some 50 cesium-clocks around the world

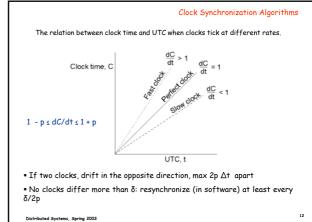
 Introduces a leap second from time to time to compensate that days are getting longer

 UTC is broadcasted through short wave radio (WWV receivers) and satellite. Satellites can give an accuracy of about ±0.5 ms

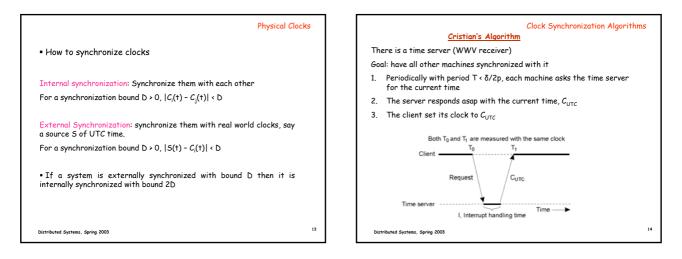
 Does this solve all our problems?

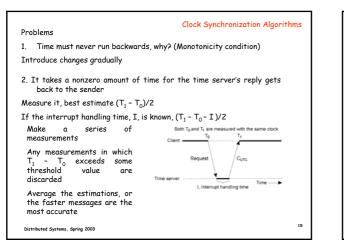


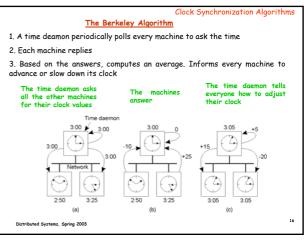
Clock Synchronization Algorithm	ns
Each machine has a timer that causes an interrupt H times per second]
A (software) clock keeps track of the number of ticks (interrupts) since some agreed-upon time in the past.	
When the timer goes off, the interrupt handler adds 1 to the software clock	
Let C be the value of the clock. Specifically, if UTC time is t, let the	
value of the clock on machine p be $C_p(t)$	
Perfect world, $C_{p}(t) = t$ for all p and t, $dC/dt = 1$	
Theoretically, a timer with H = 60, generate 216,000 (= 24*60*60) ticks per hour	
Real world, relative error 10 5,215,998 to 216,002 ticks per hour	
Maximum drift rate p:	
1 - p ≤ dC/dt ≤ 1+p	
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Physical Clocks







Clock Synchronizat	on Algorithms
A Decentralized Algorithm	New algorithms that utilize synchronized cloc
Divide time into fixed-length (R) resynchronization intervals i-th interval: $[T_0 + iR, T_0 + (i + 1)R), T_0$ some agreed-upon time in past	long to keep message numbers) Modified approach : each message carries a c
Each machine: 1. At the beginning of each interval, broadcasts its current time broadcasts will not happen precisely simultaneously, why?) 2. Starts collecting all other broadcasts that arrive during an inte 3. Runs an algorithm (e.g., average; discard m highest and m lowe average the rest) to compute a new time from them	rval 5 Any incoming message for a connection with timestamp is rejected as duplicate
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Use of Synchronized Clocks

:ks

livery, even in the face of crashes

unique message number (the server store the server crashes and reboots, also how

connection identifier (chosen by the server)

s), the server records the most recent has seen

a timestamp that is lower than the stored

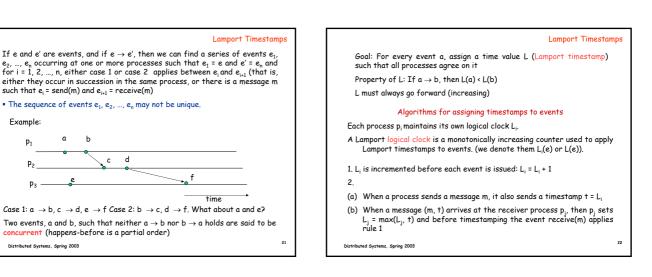
ach server maintains a variable G

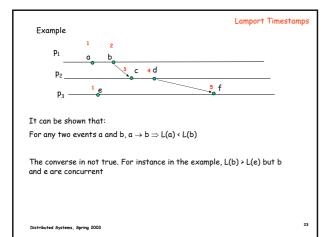
MaxClockSkew

transmission a message arrives) oound among clocks)

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Lamport Timestamps It suffices that two processes agree on the order in which events occur (no need to synchronize their clocks) The happens-before relation a happens-before b, $a \rightarrow b$: means that each process agrees that first Logical Clocks event a occurs, then afterwards event b occurs Lamport Timestamps Two cases where happens-before can be directly observed: Vector Timestamps 1. If \exists process $p_i: a \rightarrow_i b$, then $a \rightarrow b$ (that is if a and b are events in the same process, and a occurs before b then $a \rightarrow b$ is true) 2. If a is the event of a message being sent by one process and ${\sf b}$ is the event of the message being received by another process, then $a \rightarrow b$ is true. (For any message m, send(m) \rightarrow receive(m)) Transitive relation, If $a \rightarrow b$ and $b \rightarrow c$, then $a \rightarrow c$. Distributed Systems, Spring 2003 Distributed Systems, Spring 2003



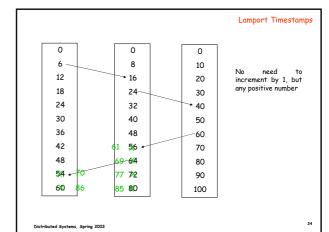


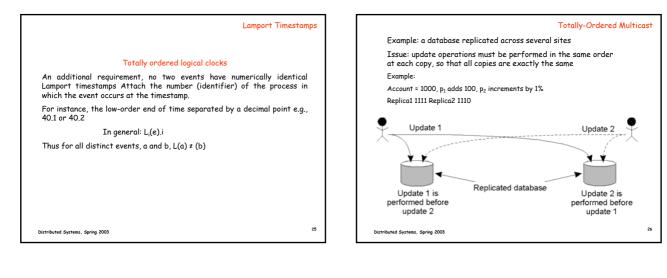
Example:

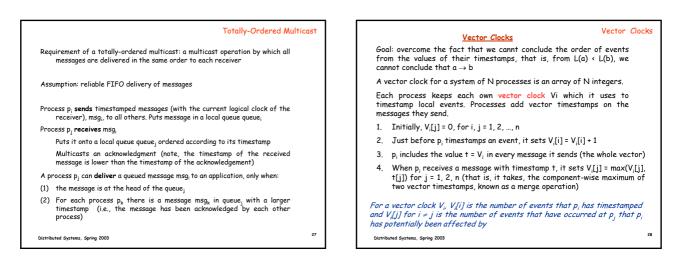
p₁

 \mathbf{p}_2

a

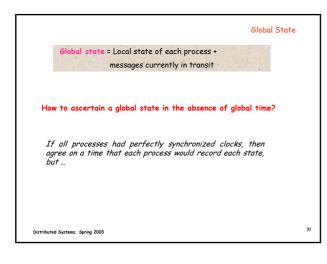






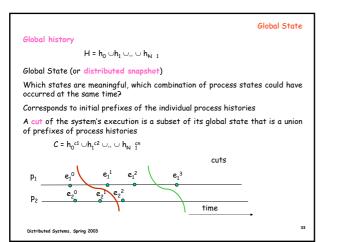
Example Vector	r Clocks
p ₁ (1, 0, 0) (2, 0, 0) a b	
P2 (2, 1, 0 C (2, 2, 0) d	
$P_3 \xrightarrow{(0, 0, 1)} e \xrightarrow{(2, 2, 2)} f$	
How to compare vector timestamps:	
V = V' iff V[j] = V'[j] for j = 1, 2,, n	
$V \leq V' \text{ iff } V[j] \leq V'[j] \text{ for } j$ = 1, 2,, n	
$V \lt V' \text{ iff } V[j] \le V'[j] \text{ and } V \neq V'$	
It can be shown that:	
For any two events a and b, $a \rightarrow b \Rightarrow L(a) < L(b)$	
The converse also holds, L(a) < L(b) \Rightarrow a \rightarrow b	
For instance in the example, b and e are concurrent which can be also concluded by the fact that neither $V(e) \leq V(b)$ nor $V(b) \leq V(e)$	I
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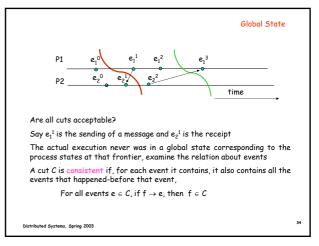


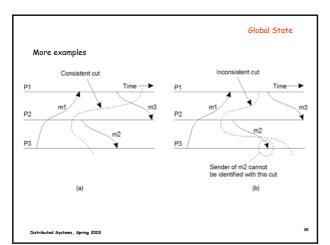


Model Assume we have N processes p_i (i = 1, 2, ..., N) Characterize each process by its history, a series of events that occur at $h_i = \langle e_i^0, e_i^1, e_i^2, ... \rangle$ Finite prefix of the history h_i^k = < e_i⁰, e_i¹, ..., e_i^k event: an internal action of the process (e.g., update of one of its state of a process p_i, s_i^k , the state of process immediately after the kth

s,0: initial state







	Global Stat	e
A consistent g	bal state is one that corresponds to a consistent cut	
The execution of the system	of a distributed system as a transition between global s	tates
	$S_0 \rightarrow S_1 \rightarrow S_2 \rightarrow \dots$	
In each transi system	ion, precisely one event occurs at some single process of	f the
	al ordering of all events in a global history that is consi: nistory's ordering	stent
	run or linearization is an ordering of the events in a g consistent with the happened-before relation on H	loba
Not all runs po	s through consistent global states, but all linearizations o	lo
A state S' is through S and	cachable from state S if there is a linearization that po S	isses
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Global State

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each process.

variables) or sending or receipt of a message

event occurred

Global State **Predicates**, **stability**, **safety and liveness**

Testing for properties amounts for evaluating a global state predicate

A global state predicate is a function that maps from the set of global states of processes in the system to {True, False}

Two interesting properties:

Suppose a is an undesirable property (e.g., deadlock)

Safety with respect to a is the assertion that a evaluates to False for all states S reachable from $S_{\rm 0}.$

Conversely, let β be a desirable property (e.g., reaching termination)

Liveness with respect to β is the property that, for any linearization L starting in state $S_0,\,\beta$ evaluates to True for some state $~S_L$ reachable from S_0

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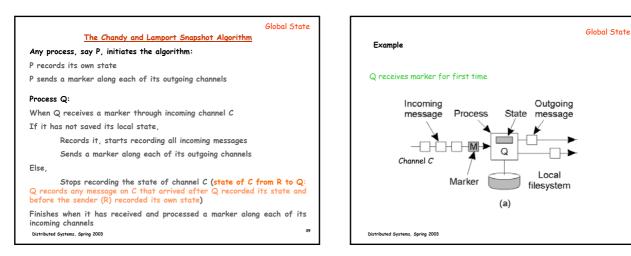
The Chandy and Lamport Snapshot Algorithm

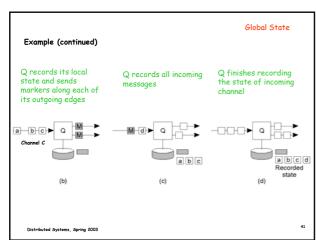
Goal: record a set of process and channel states

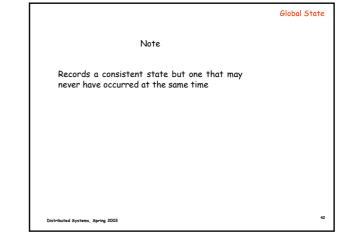
If a message has been sent by a process P but not received by a process $\mathsf{Q},$ we consider it part of the channel between them

Assumptions:

- · Neither channels nor processes fail
- · Reliable communication, any message sent is received exactly once
- Unidirectional channels, FIFO-ordered message delivery
- There is a path between any two processes
- \cdot The processes may continue their execution and send and receive messages while the snapshot algorithm takes place







Global State

Termination of the snapshot algorithm

Proof

We assume that a process that has received a marker records its state within a finite time and send markers over each outgoing channel within a finite time.

If there is a path of communication channels and processes from \textbf{p}_i to $\textbf{p}_j,$ then \textbf{p}_j will record its state a finite time after p_i recorded its state

Since the graph is strongly connected, it follows that all processes will record their states and the states of their incoming channels a finite time after some process initially records its state

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the last state recording action)

 \mathbf{S}_{snap} the recorded global state

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Let

Global State

The algorithm selects a cut from the history of execution We shall prove that this cut is consistent

Proof

Let e_i and e_j be events occurring at p_i and p_j respectively such that $e_i \rightarrow e_j$

We need to show that if e_i is in the cut then e_i is also in the cut

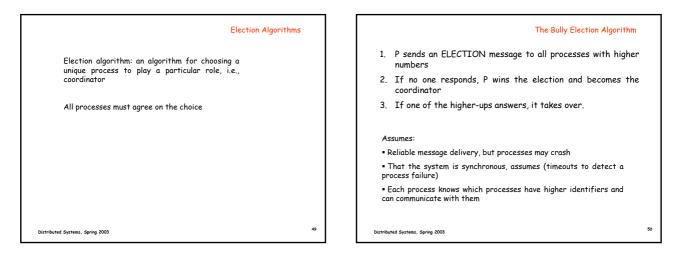
For the purposes of contradiction, assume that e_i is not in the cut, that is, p, recorded its state before e, occurred

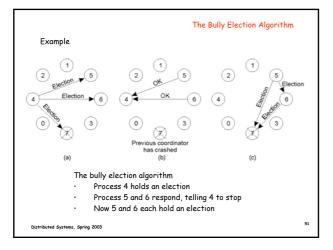
Let m1, m2, ..., mk the sequence of messages that lead to $e_i \rightarrow e_i$ By FIFO ordering, the marker from p, would have reached p, before these messages, thus p would have recorded its state before event e. This contradicts our assumption that p_i is in the cut.

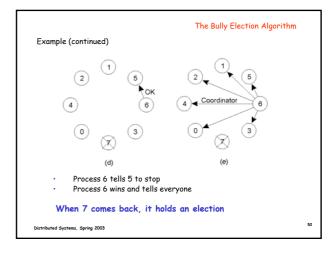
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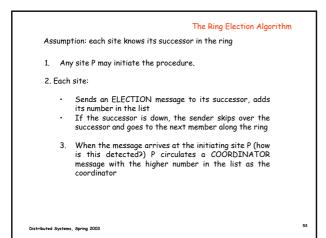
Global State Global State Proof. We shall prove a reachability relation between the observed global state and the Categorize all events in Sys as pre-snap and post-snap events initial and final states when the algorithm runs A pre-snap event at process p; is one that occurred at p; before pi recorded its state. All other post-snap. \mathbf{S}_{init} : the global state immediately before the first process recorded its state (Note a post-snap event may occur before a pre-snap event in Sys, if the two events belong to different processes) S_{final}: the global state when the snapshot algorithm terminates (immediately after Suppose $e_{j} \mbox{ is a post-snap event at one process and } e_{j*1} \mbox{ is a pre-nap event at a}$ different process: Sys = e0, e1, ... a linearization of the system as it executed (actual execution) It cannot be that $\mathbf{e}_{j} \rightarrow \mathbf{e}_{j*1} \text{(why?)}$ We shall show that there is a permutation of Sys, Sys' = e'_0 , e'_1 , e'_2 , ... such that all three states, S_{init}, S_{snap} and S_{final} occur in Sys' Thus, we can swap the two events without violating the happened-before relation We continue swapping until all pre-snap events $e_0',e_1',e_2',...,e_{R\cdot 1}'$ are ordered prior to all post-snap events $e_{R\cdot 1}',e_{R\cdot 2}',...$ $S_{snap} = e'_0, e'_1, e'_2, \dots e'_{R-1}$ actual execution (Svs) Se: . **S**..... Distributed Systems, Spring 2003

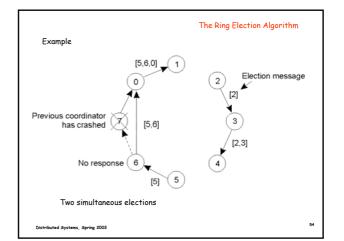
When the initiator of the recorded its state, and the distributed snapshot receives a point it had received the point it had received the marker along each of it











Mutual Exclusion

A Centralized Algorithm A Distributed Algorithm A Token-Ring Algorithm

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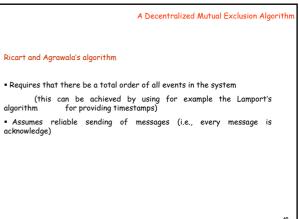
To read or update shared data structures, enter a critical region (CR) to achieve mutual exclusion In centralized systems: semaphores, monitors, etc

Mutual Exclusion	A Centralized Mutual Exclusion Algorithm
	Select one process as the coordinator
Essential requirements for mutual exclusion:	 To enter a CR, sent a <request> message to the coordinator</request>
Safety: At most one process may execute in the CR at a time	If no other process in the CR, the coordinator sends a <grant> message</grant>
Liveness: Requests to enter and exit the CR eventually succeed	Else, denies permission (e.g., does not reply and thus blocks the requesting process, or send a deny message)
Liveness implies freedom of deadlocks and starvation (indefinite postponement of entry for a process that has requested it)	 Upon exiting a CR, send a <release> message to the coordinator. The coordinator grants access to another process (e.g., takes the first item of the</release>
Absence of starvation is a fairness condition.	queue and sends a grant message)
Another fairness conditions: order in which process enter the CR	
The order that process enter the CR follows their requests to enter the	Correct (safety): Guarantees mutual exclusion?
CR:	Fair: No starvation? Order?
If one request to enter the CR happened-before another, then entry to the CR is aranted in that order	Easy to implement
The CK is granted in that order	But: the coordinator is a single point of failure & a performance bottleneck/no way to distinguish a dead coordinator from "permission denied"
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	A Centralized Mutual	Exclusion Algorithm
Example		
Process 1 asks the coordinator for permission to enter a critical region. Permission is granted	Process 2 then asks permission to enter the same critical region. The coordinator does not reply.	When process 1 exits the critical region, it tells the coordinator, when then replies to 2
0 1 2 Request OK 3 Coordinator	0 1 2 Request No reply 3 2	0 1 2 Release OK
(a)	(b)	(c)

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A Decentralized Mutual Exclusion Algorithm

When a process wants to enter the CR,

- builds a <request> message M = (CR-id, process-number, timestamp)
- sends the message to all other processes (including itself)

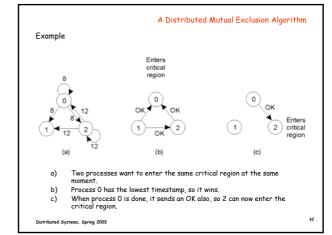
Upon receipt of a <request> message M

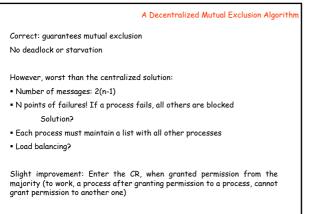
- If the receiver is not in the CR and does not want to enter the CR, replies <OK>
- ii. If the receiver is in the CR, it does nor reply, queues M
- iii. Else (the receiver is not in the CR, but wants to enter the CR),
 - Compares the timestamp with the timestamp of its own request, if lower, replies (OK), else does not reply, queues M $\,$

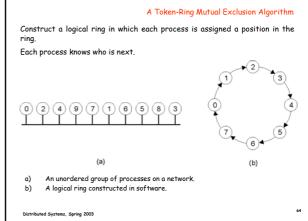
Waits till it receives OK from all processes

Upon exit from a CR,

- sends OK to all processes in its queue
- deletes them from the queue
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	A Token-Ring Mutual Exclusion Algorithm
When	the ring is initialized, process 0 is given a token.
The to	ken circulates the ring
Vhen	a process k acquires the token:
If it w	vants to enter the CR,
	it enters the CR, does all the work, leaves the region,
	passes the ring to k+1
Else,	
	it just passes the ring to k+1
	•

A Token-Ring Mutual Exclusion Algorithm	
Correctness (safety)?	
Starvation?	
Problems:	
Lost token	
Process crashes: require acknowledging the receipt of a token	
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Compar			
Algorithm	Messages per entry/exit	Client delay before entry (in message times)	Problems
Centralized	3	2	Coordinator crash
Distributed	2 (n – 1)	2 (n – 1)	Crash of any process
Token ring	1 to ∞	0 to n – 1	Lost token, process crash

Messages per entry/exit determine the bandwidth consumed

System throughput (the rate at which the collection of processes as a whole can access the critical region).

It is based on the synchronization delay between one process exiting the critical region and the next process entering it (not shown in the Table above)

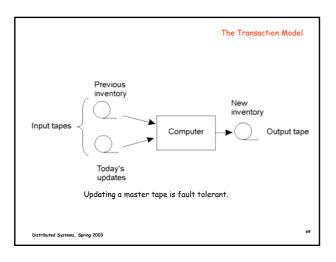
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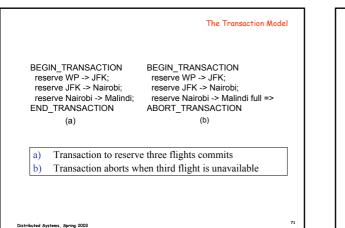
Distributed Transactions

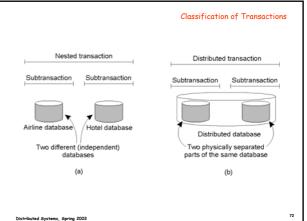
The Transaction Model Classification of Transactions Implementation **Concurrency** Control

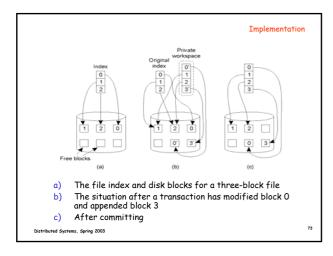
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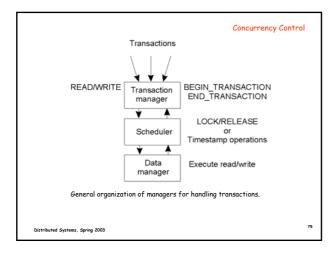
	The Transaction Model
Examples	s of primitives for transactions.
Primitive	Description
BEGIN_TRANSACTION	Make the start of a transaction
END_TRANSACTION	Terminate the transaction and try to commit
ABORT_TRANSACTION	Kill the transaction and restore the old values
READ	Read data from a file, a table, or otherwise
WRITE	Write data to a file, a table, or otherwise
The ACID properti	es
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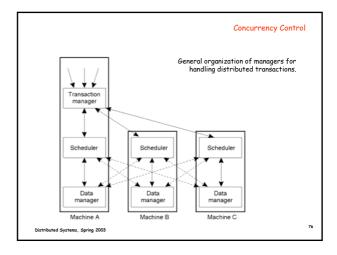






		I	nplementation
x = 0; y = 0;	Log	Log	Log
BEGIN_TRANSACTION; x = x + 1;	[x = 0 / 1]	[x = 0 / 1]	[x = 0 / 1]
y = y + 2 x = y * y;		[y = 0/2]	
END_TRANSACTION;			
(a)	(b)	(c)	(d)
a) A transaction b) - d) The log before each statement is executed			
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			Concurrenc	y Contro	I
BEGIN_TRANS	SACTION	BEGIN_TRANSACTION	BEGIN_TRANSACTION		
x = 0;		x = 0;	x = 0;		
x = x + 1;		x = x + 2;	x = x + 3;		
END_TRANSA	CTION	END_TRANSACTION	END_TRANSACTIO	ON	
(a)		(b)	(c)		
					-
Schedule 1	x = 0; x = x + 1; x = 0; x = x + 2; x = 0; x = x + 3			Legal	
Schedule 2	x = 0; x = 0; x = x + 1; x = x + 2; x = 0; x = x + 3;			Legal	
Schedule 3	x = 0; x =	0; x = x + 1; x = 0; x = x + 2;	; x = x + 3;	lllegal	
					-
		(d)			
a) - c) T	hree transa	ctions T1, T2, and T3			
	le schedule				
, i i i i i i i i i i i i i i i i i i i					
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